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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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**Assignee:** Samsung Electronics Co., Ltd.

**Title:** THIN FILM TRANSISTOR ARRAY PANEL AND MANUFACTURING METHOD THEREOF

**Application No.:** 10/759,389      **Filing Date:** 01/16/2004

**Confirmation No.:** 6707      **Date Allowed:** 12/13/206

**Examiner:** Ton, Minh Toan T.      **Group Art Unit:** 2871

**Docket No.:** AB-1351 US

San Jose, California  
March 15, 2007

Mail Stop Petition  
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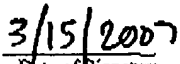
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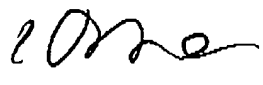
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Sheet 1 of 1

U.S. Department of Commerce, Patent and Trademark Office				Atty. Docket No.		Application No.	
				AB-1351 US.		10/759,389	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Applicant(s)			
(Use several sheets if necessary)				Jun-Hyung SOUK, et al.			
				Filing Date		Group	
				01/16/2004		2871	
U.S. Patent Documents							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	2002/0074549 A1	06/20/2002	Park et al.			
	AB						
	AC						
	AD						
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	AH						
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	AJ						
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Foreign Patent Documents							
		Document	Date	Country	Class	Subclass	Translation Yes No
	AL	1384394	12/11/2002	China			
	AM						
	AN						
	AO						
	AP						
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
	AQ	English Language Abstract, CN Patent First Publication No. 1384394, 12/11/2002, 1 page.					
	AR	English version of Chinese Office Action corresponding to Chinese Patent Application No. 2004100000993.9, 7 pages.					
	AS						
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